

**Features**

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

**Typical Applications**

- AC controllers
- DC and AC motor control
- Controlled rectifiers

**$I_{T(AV)}$  2000A**  
 **$V_{DRM}/V_{RRM}$  1100~1800V**  
 **$I_{TSM}$  30 kA**  
 **$I^2t$  4500 10<sup>3</sup>A<sup>2</sup>S**



SYMBOL	CHARACTERISTIC	TEST CONDITIONS		T <sub>J</sub> (°C)	VALUE			UNIT
					Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled,	T <sub>C</sub> =70°C	125			2000	A
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage Repetitive peak reverse voltage	tp=10ms		125	1100		1800	V
$I_{DRM}$ $I_{RRM}$	Repetitive peak current	at $V_{DRM}$ at $V_{RRM}$		125			120	mA
$I_{TSM}$	Surge on-state current	10ms half sine wave		125			30	kA
$I^2t$	$I^2t$ for fusing coordination	$V_R=0.6V_{RRM}$					4500	A <sup>2</sup> s*10 <sup>3</sup>
$V_{TO}$	Threshold voltage			125			0.98	V
$r_T$	On-state slope resistance						0.15	mΩ
$V_{TM}$	Peak on-state voltage	$I_{TM}=4000A, F=28kN$		25			1.58	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$		125			1000	V/μs
di/dt	Critical rate of rise of on-state current	$V_{DM}=67\%V_{DRM}$ to2500A, Gate pulse $t_r \leq 0.5\mu s$ $I_{GM}=1.5A$		125			200	A/μs
$Q_{rr}$	Recovery charge	$I_{TM}=2000A, tp=2000\mu s, di/dt=-20A/\mu s,$ $V_R =50V$		125		1600		μC
$I_{GT}$	Gate trigger current			25	40		300	mA
$V_{GT}$	Gate trigger voltage	$V_A=12V, I_A=1A$			0.8		3.0	V
$I_H$	Holding current				20		300	mA
$V_{GD}$	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$		125	0.3			V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine double side cooled Clamping force 28.0kN					0.016	°C/W
$R_{th(c-h)}$	Thermal resistance case to heatsink						0.004	
$F_m$	Mounting force				21		30	kN
$T_{stg}$	Stored temperature				-40		140	°C
$W_t$	Weight					590/ 640		g
Outline	P12							

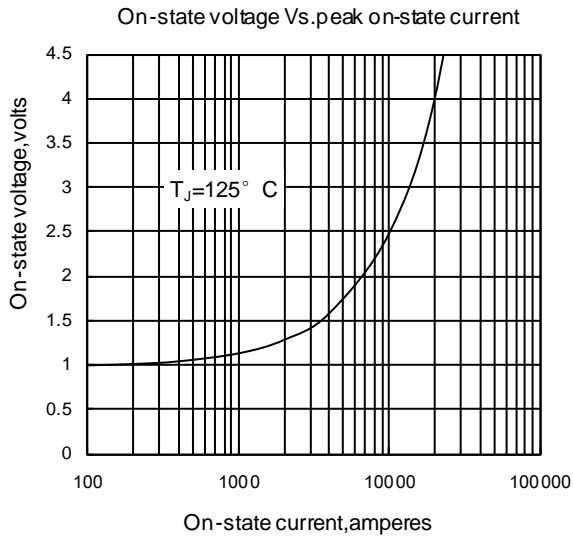


Fig1

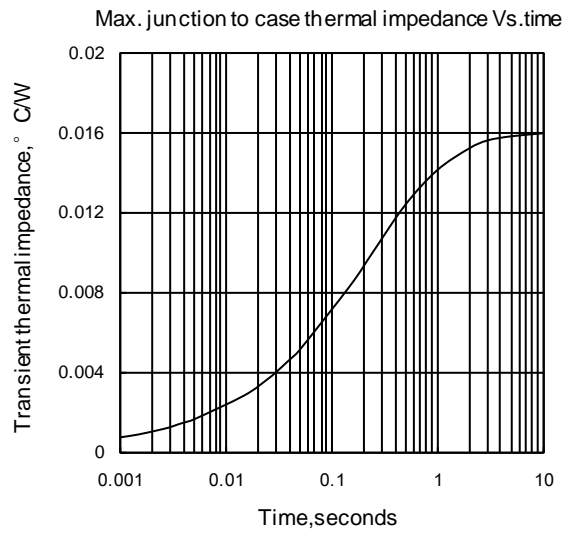


Fig2

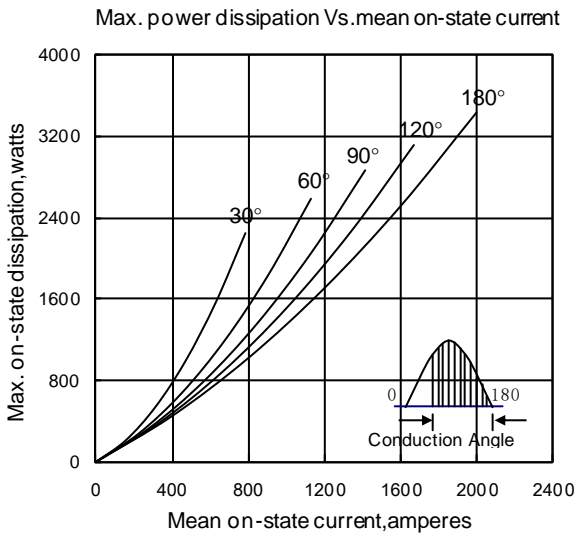


Fig3

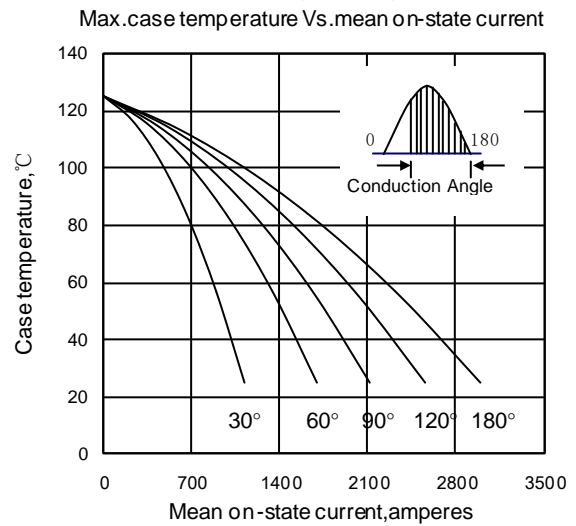


Fig4

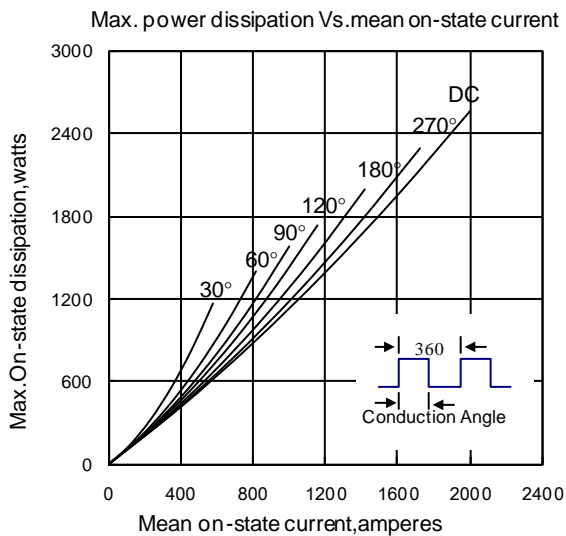


Fig5

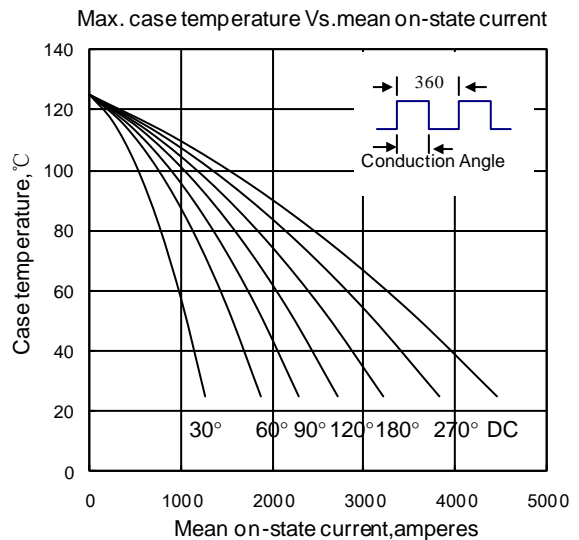


Fig6

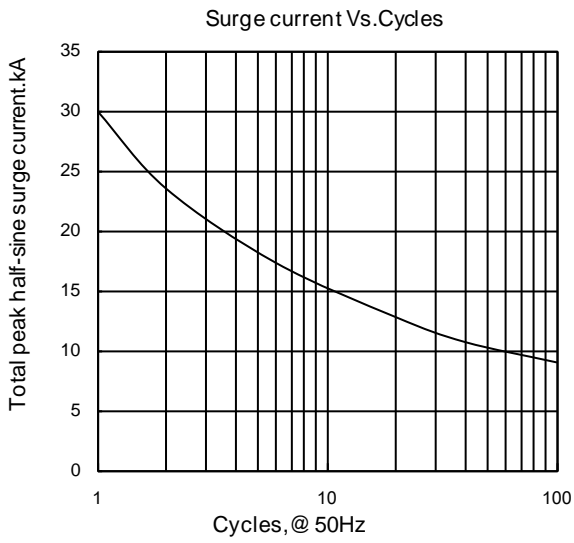


Fig7

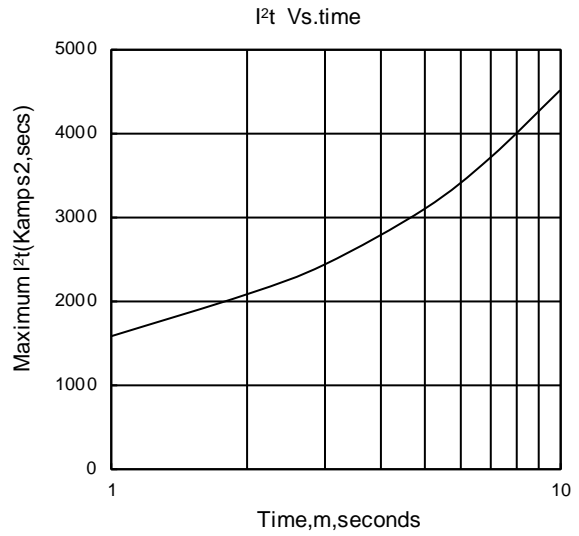


Fig8

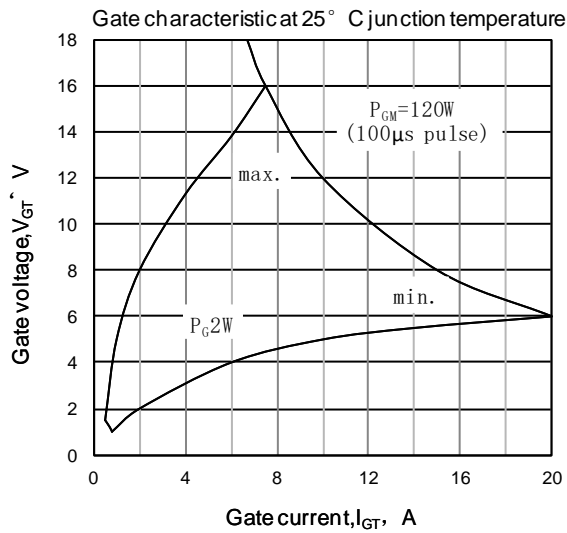


Fig9

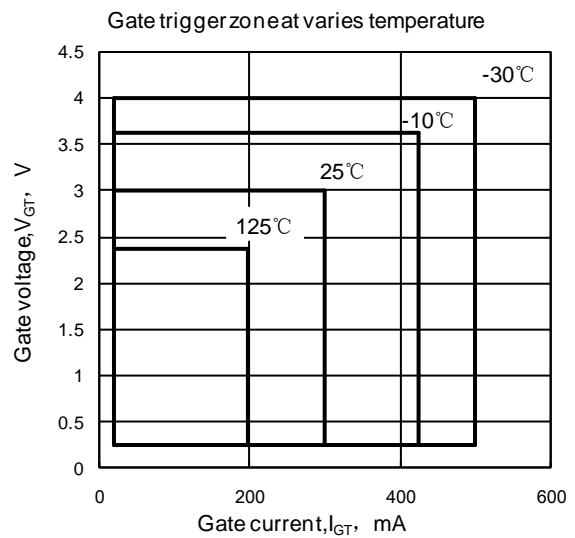


Fig10

